

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

**PRELIMINARY AMENDMENT ACCOMPANYING APPLICATION**

APPLICANT: Yoshiyuki Yanagisawa, et al. ATTY. DOCKET NO. 09792909-5046

SERIAL NO.

DATE FILED:

INVENTION: "ASSEMBLY JIG AND MANUFACTURING METHOD OF  
MULTILAYER SEMICONDUCTOR DEVICE"

Assistant Commissioner of Patents  
Washington, D.C. 20231

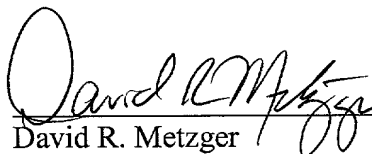
S I R:

Between the title and the heading "Background of the Invention" on page 1, insert the following:

--RELATED APPLICATION DATA

The present application claims priority to Japanese Application No. P2000-171059 filed June 7, 2000, which application is incorporated herein by reference to the extent permitted by law.--

Respectfully submitted,

 (Reg. No. 32,919)  
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